

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

Applicant(s):	G.Y. TSAO et al.	Examiner	Gopal C. Ray
Serial No.	10/816,435	Group Art Unit	2111
Filed	March 31, 2004	Docket No.	P19002
TITLE	INTERRUPT SCHEME FOR AN INPUT/OUTPUT DEVICE		

---

CERTIFICATE UNDER 37 CFR 1.8:

I hereby certify that this correspondence is being transmitted through the USPTO EFS-Web system over the Internet to Gopal C. Ray of the U.S. Patent and Trademark Office on January 17, 2007.

/Janaki K. Davda/  
Janaki K. Davda Reg. No. 40,684

**REQUEST FOR PLACEMENT OF DOCUMENTS IN FILE**

Dear Examiner:

Applicants would like to request placement of the following two documents in the file of this case:

- 1) U.S. Patent No. 6,750,870, issued on June 15, 2004, to Olarig.
- 2) "Developing a Third Generation I/O Specification", by Pat Correia, Intel®

DeveloperUPDATE Magazine, March 2002, pages 1-4. Applicants are submitting a copy of the "Developing a Third Generation I/O Specification" document along with this Request.

Based on a review of the documents, it is believed that the documents are not material to patentability of any existing claim. However, Applicants believe that the public interest is best served by placing these documents in the file.

Dated: January 17, 2007

By: /Janaki K. Davda/ \_\_\_\_\_

Janaki K. Davda  
Registration No. 40,684

Please direct all correspondences to:

David Victor  
Konrad Raynes & Victor, LLP  
315 South Beverly Drive, Ste. 210  
Beverly Hills, CA 90212  
Tel: 310-553-7977  
Fax: 310-556-7984